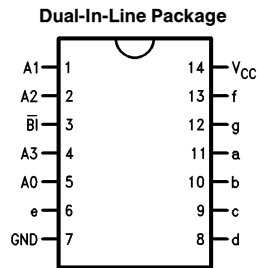


## 54LS49 BCD to 7-Segment Decoder

### General Description

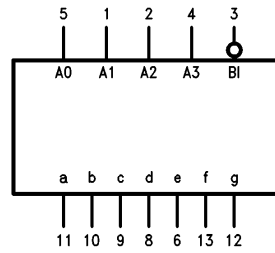
The 54LS49 translates four lines of BCD (8421) input data into the 7-segment numeral code as shown in the Function Table. It has open-collector outputs and is logically the 14-pin version of the '48, without the lamp test and ripple blanking features. Also see the 'LS249 data sheet.

### Connection Diagram



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### Logic Symbol



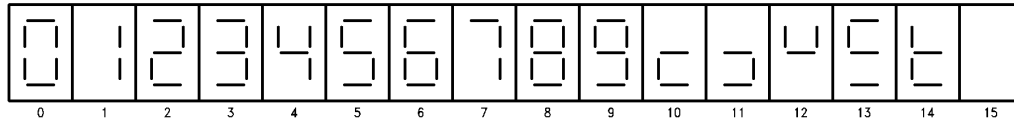
TL/F/10204-2

**Order Number 54LS49DMQB or 54LS49FMQB**  
**See NS Package Number J14A or W14B**

V<sub>CC</sub> = Pin 14  
 GND = Pin 7

Pin Names	Description
A0-A3	BCD Inputs
$\overline{\text{BI}}$	Blanking Input (Active LOW)
a-g	Segment Outputs (Active HIGH)

### Numerical Designations—Resultant Displays



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## Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	5.5V
Operating Free Air Temperature Range	
54LS	−55°C to +125°C
Storage Temperature Range	−65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

## Recommended Operating Conditions

Symbol	Parameter	54LS49			Units
		Min	Nom	Max	
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	V
V <sub>IH</sub>	High Level Input Voltage	2			V
V <sub>IL</sub>	Low Level Input Voltage			0.7	V
I <sub>OH</sub>	High Level Output Current			250	μA
I <sub>OL</sub>	Low Level Output Current			4	mA
T <sub>A</sub>	Free Air Operating Temperature	−55		125	°C

## Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 1)	Max	Units
V <sub>I</sub>	Input Clamp Voltage	V <sub>CC</sub> = Min, I <sub>I</sub> = −18 mA			−1.5	V
V <sub>OH</sub>	High Level Output Voltage	V <sub>CC</sub> = Min, I <sub>OH</sub> = Max, V <sub>IL</sub> = Max, V <sub>IH</sub> = Min	2.5			V
V <sub>OL</sub>	Low Level Output Voltage	V <sub>CC</sub> = Min, I <sub>OL</sub> = Max, V <sub>IH</sub> = Min, V <sub>IL</sub> = Max			0.4	V
I <sub>I</sub>	Input Current @ Max Input Voltage	V <sub>CC</sub> = Max, V <sub>I</sub> = 10.0V			0.1	mA
I <sub>IH</sub>	High Level Input Current	V <sub>CC</sub> = Max, V <sub>I</sub> = 2.7V			20	μA
I <sub>IL</sub>	Low Level Input Current	V <sub>CC</sub> = Max, V <sub>I</sub> = 0.4V			−0.4	mA
I <sub>CC</sub>	Supply Current	V <sub>CC</sub> = Max			15	mA

Note 1: All typicals are at V<sub>CC</sub> = 5V, T<sub>A</sub> = 25°C.

Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.

## Switching Characteristics V<sub>CC</sub> = +5.0V, T<sub>A</sub> = +25°C (See Section 1 for waveforms and output load)

Symbol	Parameter	54LS		Units
		C <sub>L</sub> = 15 pF		
		Min	Max	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay; R <sub>L</sub> = 2 kΩ A <sub>n</sub> to a−g		100 100	ns
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay; R <sub>L</sub> = 6 kΩ $\overline{B}$ I to a−g		100 100	ns

## Function Table

Decimal or Function	Inputs					Outputs							Note
	A3	A2	A1	A0	$\overline{BI}$	a	b	c	d	e	f	g	
0	L	L	L	L	H	H	H	H	H	H	H	L	1
1	L	L	L	H	H	L	H	H	L	L	L	L	
2	L	L	H	L	H	H	H	L	H	H	L	H	
3	L	L	H	H	H	H	H	H	H	L	L	H	
4	L	H	L	L	H	L	H	H	L	L	H	H	
5	L	H	L	H	H	H	L	H	H	L	H	H	
6	L	H	H	L	H	L	L	H	H	H	H	H	
7	L	H	H	H	H	H	H	H	L	L	L	L	
8	H	L	L	L	H	H	H	H	H	H	H	H	
9	H	L	L	H	H	H	H	H	L	L	H	H	
10	H	L	H	L	H	L	L	L	H	H	L	H	
11	H	L	H	H	H	L	L	H	H	L	L	H	
12	H	H	L	L	H	L	H	L	L	L	H	H	
13	H	H	L	H	H	H	L	L	H	L	H	H	
14	H	H	H	L	H	L	L	L	H	H	H	H	
15	H	H	H	H	H	L	L	L	L	L	L	L	
BI	X	X	X	X	L	L	L	L	L	L	L	L	2

**Note 1:** The blanking input must be open or held at a HIGH level when output functions 0 through 15 are desired.

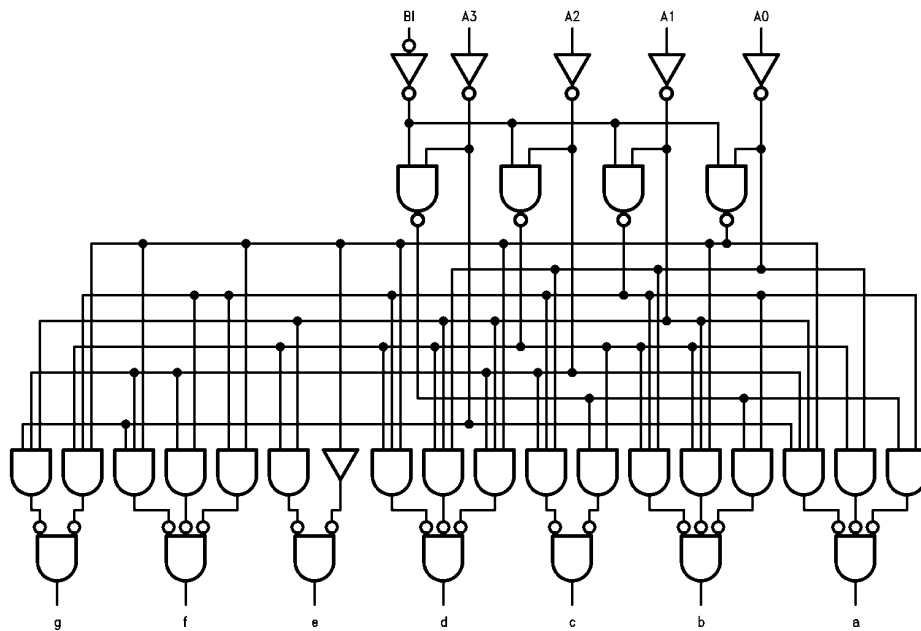
**Note 2:** When a LOW level is applied to the blanking input all segment outputs go to a LOW level regardless of the state of any other input condition. X = Input may be HIGH or LOW.

H = HIGH Voltage Level

L = LOW Voltage Level

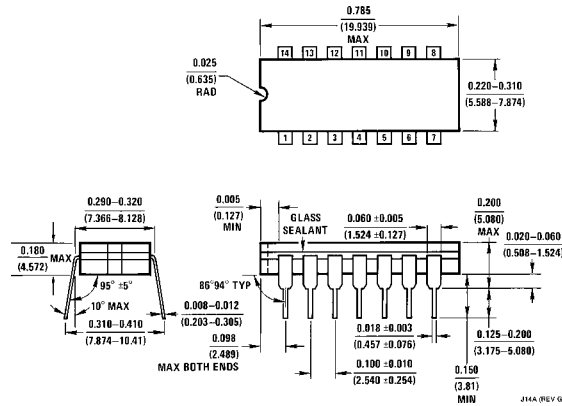
X = Immaterial

## Logic Diagram

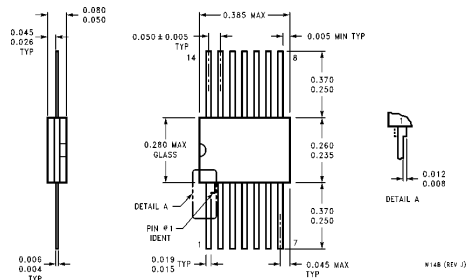


TL/F/10204-4

**Physical Dimensions** inches (millimeters)



**14-Lead Ceramic Dual-In-Line Package (J)**  
**Order Number 54LS49DMQB**  
**NS Package Number J14A**



**14-Lead Ceramic Flat Package (W)**  
**Order Number 54LS49FMQB**  
**NS Package Number W14B**

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